LMU CHANGELOG

# Release 0.0.1

**GIT Commit**: [] **Date**: September 11, 2024

**Author**: BRANCARO\p.parrino <p.parrino@brancaro.com>

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# Release 0.0.1

**GIT Commit**: [0fd5e53] **Date**: September 9, 2024

**Author**: BRANCARO\p.parrino <p.parrino@brancaro.com>

LMU first released:

* Created LMU project with MPLAB X

- Set Clock @180MHz from 8MHz External Oscillator and with PLL

- Set TMR1 Interrupt @250us

- Set UART1 to Baud Rate:115200, Parity:None, Data Bits:8, Stop Bits:1, Parity Error:None