LMU CHANGELOG

# Release 0.0.1

**GIT Commit**: [] **Date**:

**Author**: BRANCARO\p.parrino [p.parrino@brancaro.com](mailto:p.parrino@brancaro.com)

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# Release 0.0.1

**GIT Commit**: [c4142b4] **Date**: October 23, 2024

**Author**: BRANCARO\p.parrino [p.parrino@brancaro.com](mailto:p.parrino@brancaro.com)

- - Fixed WARNING about " #pragma config OSCIOFNC = OFF //OSC2 Pin Function bit->OSC2 is clock output" There was a conflict with Bootloader and LMU application settings

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# Release 0.0.1

**GIT Commit**: [638870e] **Date**: October 22, 2024

**Author**: BRANCARO\p.parrino [p.parrino@brancaro.com](mailto:p.parrino@brancaro.com)

- Implemented HW\_HeartBeat module

- Renamed Digital Input / Output name pins with DI\_ and DO\_ prefix

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# Release 0.0.1

**GIT Commit**: [7e5616a] **Date**: October 21, 2024

**Author**: BRANCARO\p.parrino [p.parrino@brancaro.com](mailto:p.parrino@brancaro.com)

- Implemented LMU BootLoader

- Added UnifiedHostApp Microchip application associated to BootLoader to download the LMU application SW into LMU board

- Updated LMU SW tho handle BooLoader functionalities

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# Release 0.0.1

**GIT Commit**: [1256194] **Date**: October 07, 2024

**Author**: BRANCARO\p.parrino [p.parrino@brancaro.com](mailto:p.parrino@brancaro.com)

- Added calculation functions for all ADCs on Override + Logic Board

- Added Maintenance pins for manage hw heartbeat. (temporary in LED\_OnBoard module)

- Added PEAKCAN LMU project for data CAN management

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# Release 0.0.1

**GIT Commit**: [0286331] **Date**: October 01, 2024

**Author**: BRANCARO\p.parrino [p.parrino@brancaro.com](mailto:p.parrino@brancaro.com)

- Improved and tested ADCManager module

- Improved CANFeddback module

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# Release 0.0.1

**GIT Commit**: [13f2e7e] **Date**: September 27, 2024

**Author**: BRANCARO\p.parrino [p.parrino@brancaro.com](mailto:p.parrino@brancaro.com)

- Implemented ADCManager module

- Implemente ADC module

- Implemented I2C module

- Implemented basic structure for MCP23017\_IO\_EXP module

- Implemented basic structure for PCA9685\_IO\_PWM module

- Implemented basic structure for ADS7128\_ADC\_EXP module

- Improved CANFeedback module

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# Release 0.0.1

**GIT Commit**: [cbeb507] **Date**: September 25, 2024

**Author**: BRANCARO\p.parrino [p.parrino@brancaro.com](mailto:p.parrino@brancaro.com)

- Implemented CANFeedback module

- Moved CAN HeartBeat function in CANFeedback module

- Implemented I2C driver

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# Release 0.0.1

**GIT Commit**: [7e6e94a] **Date**: September 24, 2024

**Author**: BRANCARO\p.parrino <p.parrino@brancaro.com>

- Implemented CAN module

- Implemented CAN RecvdMsgParser module (with example test for Tx and Rx CAN messages)

- Improvements in LED\_OnBoard module and Version module

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# Release 0.0.1

**GIT Commit**: [707e293] **Date**: September 12, 2024

**Author**: BRANCARO\p.parrino <p.parrino@brancaro.com>

- Implemented Version module

- Updated .gitignore file: removed 'nbproject' and 'dist' folders

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# Release 0.0.1

**GIT Commit**: [d473c92] **Date**: September 11, 2024

**Author**: BRANCARO\p.parrino <p.parrino@brancaro.com>

- Implemented module App\_Main to manage the project scheduler

- Implemented module Timers to have timers functionalities

- Implemented module LED On Board to manage on board leds

- Implemented hearbeat for On Board Green LED- Added LMU ChangeLog.docx file

- Modified .gitignore file to remove from backup .generated\_files folder

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# Release 0.0.1

**GIT Commit**: [0fd5e53] **Date**: September 9, 2024

**Author**: BRANCARO\p.parrino <p.parrino@brancaro.com>

LMU first released:

* Created LMU project with MPLAB X

- Set Clock @180MHz from 8MHz External Oscillator and with PLL

- Set TMR1 Interrupt @250us

- Set UART1 to Baud Rate:115200, Parity:None, Data Bits:8, Stop Bits:1, Parity Error:None